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Abstract

Nowadays, an electronic nose becomes an important tool for detecting gas. The electronic nose consists of gas sensor array combined with neural networks to recognize patterns of the sensor array. Currently, the implementation of the neural network on the electronic nose systems still use personal computer so that less flexible or not portable. This paper discusses the electronic nose system implemented in a Field Programmable Gate Array (FPGA). The sensor array consists of eight Quartz Crystal Microbalance (QCM) coated with chemical materials. The eight channel-frequency counter is used to measure the frequency change of the sensor due to the presence of gas adsorbed to the surfaces. The bipolar sigmoid activation function used in the neuron model is approximated by a second order equation. The experimental result showed that the electronic nose system could recognize all the types of gas with 92% success rate.

Keywords: electronic nose system, FPGA, neural network, QCM

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1. Introduction

The electronic nose system has been widely applied in broad variety of fields, such as for monitoring coffee quality [1], plant disease diagnosis [2], fruit classification [3] and air monitoring [4]. This system consists of a sensor array combined with pattern recognition algorithm to identify the gas. Currently, electronic nose systems still use computers [5], which are less flexible and are not portable.

In recent years, there have been studied several gas sensors with different working principles, such as electro-chemical sensors [6], metal oxide semiconductor sensors [7, 8], ratio metric fluorescence sensors [9], fiber optic sensors [10, 11], and Quartz Crystal Microbalance (QCM) [12, 13]. Compared with other gas sensors, QCM sensors are simple, cheap, small, and high sensitive to the weight of molecules.

Field Programmable Gate Array (FPGA) has recently expanded the structure with the presence of digital signal processing [14, 15]. FPGA technology makes it easy to reconfigurable, reprogrammable with low cost advantages, and easy to integrate [16]. In many applications, FPGA can be used in implementing pattern recognition algorithms, such as SOM-Hebb [17], Learning Vector Quantization (LVQ) [18, 19], and neural networks [20, 21]. In neural network architecture, there are several activation functions, such as sigmoid equation. When implementing the sigmoid function in the FPGA, it often has constraints in hardware implementation. Some previous studies have approached the sigmoid function, such as with Lookup Table (LUT) combinations using linear interpolation [22], exponential function [23], the 4th order Taylor series [24], and second order function [25].

Several studies have implemented electronic nose on FPGA, including the implementation of metal oxide semiconductor gas sensors equipped with Multilayer perceptron (MLP) with single multiple inputs multiple outputs (SMIMO) and multiple multiple inputs single

output (MMISO) [26]. Another study uses gas sensor array with principal component analysis (PCA) as a pattern recognition method [27]. In this study, we have implemented electronic nose on FPGA using polymer coated QCM sensor array. Some of the gases used as sample material are kerosene, gasoline, ammonia, and ethanol. The second order function model is used as an activation function in the neural network pattern recognition.

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2. Research Method

Quartz resonator sensor has high sensitivity to the gas molecules adsorbed to the electrodes shown in Figure 1. This will change the oscillation frequency of the quartz resonator, defined as (1):

$$\Delta f = -\left(\frac{2f^2}{\rho_{SiO_2} vA}\right) \Delta m \tag{1}$$

where Δf is the observed frequency shift (Hz), Δm is the mass change per unit area (g/cm²), ρ is the crystal density, v is the velocity of acoustic wave propagation in quartz crystal, A is the surface area (cm²).

Gas Molecules

Figure 1. The basic principle of QCM sensor

The design of the electronic nose consists of an array of QCM sensors, oscillator circuits, 1-second time base, frequency counters, latches, neural network, and LCD driver shown in Figure 2.The Pierce oscillator accompanied by a QCM sensor is used as a gas sensor unit. There are eight QCMs coated with various chemicals including OV-17, PEG-20M, OV-225, OV-25, PEG-200, OV-101, PEG-1540, and PEG-6000. The time base of one second is obtained from the delay process for the 50 Mhz oscillator circuit attached on the FPGA module.

By using a time delay of one second, it allows to measure the frequency change with a resolution of 1 Hz. The frequency counter block can be constructed from several flip-flop circuits, such as D, RS, or JK Flip-flops. In VHDL code, frequency counter is designed with 24 bits using behaviour architecture.

The neural network architecture used in this study consists of three layers shown in Figure 3. This network consists of 8 nodes in the input layer, 10 neurons in the hidden layer, and 2 neurons in the output layer. Generally, the supervised neural network algorithms are using MLP with back propagation learning method. The hidden neuron is expressed by:

$$z_{net_{j}} = v_{0j} + \sum_{i=1}^{n} x_{i} v_{ij}$$

$$z_{j} = f_{1}(z_{net_{j}}) = \frac{1 - e^{-z_{net_{j}}}}{1 + e^{-z_{net_{j}}}}$$
(2)

the neuron output is expressed by:

$$y_{n}net_{k} = w_{0k} + \sum_{j=1}^{p} z_{j} \cdot w_{jk}$$
$$y_{k} = f_{2}(y_{n}net_{k}) = \frac{1 - e^{-y_{n}net_{k}}}{1 + e^{-y_{n}net_{k}}}$$
(3)





Figure 2. Block diagram of the electronic nose system used in the experiment



Figure 3. The feedforward neural network architecture

Xilinx ISE software is used in synthesis and compilation of VHDL code. Selection of data types affects the compilation performance. The compilation process will have many problems when the (2) and (3) are implemented in VHDL code. The second order equation is an easy approach to replace non-linear functions such as the sigmoid function. The transition from lower the and upper saturation regions of the sigmoid function can be replaced by [25]:

$$H(z) = \begin{cases} z(\beta - \theta z) for \ 0 < z < L \\ z(\beta + \theta z) for - L < z < 0 \end{cases}$$
(4)

where, β and θ are used to adjust the slope and gain of non-linear functions. The equation can be written as:

$$G(z) = \begin{cases} 1 & forz > L \\ H(z)for - L < z < L \\ -1 & forz < -L \end{cases}$$
(5)

to approximate the sigmoid equation, we get $\theta = 1/L^2$ and $\beta = 2/L$, with L = 4 and then a slightly modify the equation to be:

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$$G(z) = \begin{cases} 1 & \text{for } z > 4 \\ H(z) \text{for } -4 < z < 4 \\ -1 & \text{for } z < -4 \end{cases}$$
 where, $H(z) = \begin{cases} 0.52 \ z - 0.07 \ z^2 \text{for } 0 < z < 4 \\ 0.52 \ z + 0.07 \ z^2 \text{for } -4 < z < 0 \end{cases}$ (6)

An integer type is suggested in this design to approach floating-point operation. Therefore, all numbers need to be changed in the form of integer values with multiplying by 1024, which the steps can be written as:

Normalization of the inputs $x_1, x_2, ..., x_n$ are represented by: $x_{norm} = \frac{x \times 1024}{x_{max}}$ a. x_{max}

- b.
- The weight and bias values v_{11} , v_{12} ... v_{ij} ; w_{11} , w_{12} ... w_{jk} are multipled by 1024. The hidden neuron: $z_{net} = \frac{\sum input(x_{norm}) \times weights(v_{ij})}{1024} + bias$ 1024

$$z = G(z_{net})$$

with the sigmoid equation (G) is represented by:

$$G(g_{net}) = \begin{cases} \frac{1024 \; ; \; for g_{net} > 4000}{\left(\frac{532 \times g_{net} - \frac{72 \times g_{net}^2}{1024}}{1024}\right)}; for \quad 0 \leq g_{net} < 4000}{\left(\frac{532 \times g_{net} + \frac{72 \times g_{net}^2}{1024}}{1024}\right)}; for - 4000 \leq g_{net} < 0}{-1024 \; ; \; for g_{net} < -4000} \end{cases}$$
(7)

The output neuron: $y_{net} = \frac{\sum z \times weights (w_{jk})}{y = G(y_{net})} + bias$ d.

the VHDL code of the neurons can be expressed as follows:

if ($z_{net} < 0$ and $z_{net} > -4000$) then $z \le (532^* z_{net} + (72^* z_{net}^* z_{net})/1024)/1024$; elsif (z_{net} < -4000) then z<= -1024; elsif (z_{net} >= 0 and z_{net} < 4000) then z<= (532* z_{net} - (72* z_{net} * z_{net})/1024)/1024; else z<= 1024; end if:

the VHDL code for the LCD driver follows the LCD vendor's instructions. Hitachi LCD is used on this system with the two 4-bit operations of data transfer. In order to connect to the Input/Output ports of the FPGA, the result of the each block must be converted into a standard logic vector type.

3. Results and Analysis

The electronic nose system has been implemented on Spartan 3E XC3S1600E-5FG320 starter kit board. The FPGA module consists of two RS232 ports, a VGA port, 2x16 LCD, 10/100 Ethernet PHY, a 40 pin Hirose FX2 connector, 29,504 flip flops, 29.504 LUTs, 32M x 16 DDR SDRAM and thirty-six of multiplier 18x18. Prior to applying in the real system, the frequency counter is simulated on Xilinx ISE Software. The measurement of the frequency counter hardware is performed for 8-channels. The experimental result shows that this counter unit has an inter-channel cross talk of 0.0002%. The more channels there will be more interference between channels. In previous study, it was only used 2-channels with precision and stability of 1 Hz with TCXO oscillator [28].

Figure 4 (a) shows a comparison between the actual bipolar sigmoid and the second order functions; while Figure 4 (b) shows the bipolar sigmoidmultiplied by 1000 and the second order multiplied by 1024. The average error for the second order and the modified second order are 1.7% and 2.4%, respectively, as shown in Figure 4 (c). The results of the implementation of the second order function indicate that the number of Slices is 143, the number of 4 inputs of LUTs is 274, and the number of IOBs is 10. The summary of device utilization is still greater at 27%, and 31% for slices and LUTs, respectively, due to the implementation of LCD drivers. However, the use of 84% of IOBs is less than previous study using fixed-point arithmetic simulated in ModelSim [29].



Figure 4. The comparison of sigmoid function with (a) second order, (b) modified second order, and (c) error plots of the both approaches

For data analysis, this system requires a computer to display the response of each sensor in real time shown in Figure 5. Figure 6 shows the patterns of the sensor array for any gases with different concentrations. The results of this experiment indicate the similarity of sensor pattern under varied gas concentration. Each gas provides a typical pattern of sensor response. These response patterns are then used as inputs on the neural network to recognize the type of gas.



Figure 5. The time response of QCM sensors exposed to ammonia vapor

The training phase of the neural network is performed on a computer using backpropagation algorithm. For the iterations of 100,000 epochs, the network has a Mean Square Error (MSE) of 0.00005. The weights of the training phase are entered into the FPGA. The overall use of components in Slice of Flip Flop is 71% of 29,040; 4 inputs LUT is 68% of 29,040; Multiplier 18x18 is 75% of 36; and buffermultiplex is 41% of 24. In the testing phase, the neural network is introduced with forty data sets for all types of gases. The experimental result shows that the neural network has an average identification rate of 92% as shown in Table 1.





θ1.	. Ine	Identification F	ate of the Neural I	ve
	No	Gas Samples	Success Rate (%)	
	1	Gasoline	100	-
	2	Ammonia	100	
	3	Kerosene	80	
	4	Ethanol	90	
_		Average	92	_

Table 1. The Identification Rate of the Neural I	Network
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4. Conclusion

The electronic nose system based on a QCM sensor array and neural network pattern recognition has been constructed and implemented on the XC3S1600e FPGA board. The bipolar sigmoid activation function used in the neuron model is approximated by a second orderfunction with an error rate of 2.4%. The overall design of the system requires the use of

slices and LUTs of 71%, and 68%, respectively. The results of the experiment indicate the similarity of sensor pattern to varied gas concentration. Each gas provides a typical pattern of sensor response. The neural network could recognize all the gases tested in the experiments with an identification rate of 92%. The use of the counter circuits in this experiment requires a sampling time of 1 second. To increase the respond time, a reciprocal frequency counter will be developed in this electronic nose system. The graphical LCD will be implemented to display the data in detail.

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