Numerical Simulation of Tunneling Current in an Anisotropic Metal-Oxide-Semiconductor Capacitor

 Fatimah A. Noor*, Ferry Iskandar, Mikrajuddin Abdullah, Khairurrijal[¥]

 Physics of Electronic Materials Research Division, Faculty of Mathematics and Natural Sciences, Institut Teknologi Bandung

 Jalan Ganesa 10, Bandung 40132, Indonesia, telp/fax: +622 2500834/+622 2506452

 e-mail: fatimah @fi.itb.ac.id*

Abstrak

Pada makalah ini telah dikembangkan sebuah model arus terobosan yang melalui kapasitor MOS dielektrik ganda dengan melibatkan massa anisotropik. Transmittansi dihitung secara numerik dengan menggunakan metode transfer matriks dan menyertakan efek kopling antara energi longitudinal dan kinetik yang diwakili oleh kecepatan fasa elektron di gerbang. Transmittansi yang diperoleh kemudian digunakan untuk menghitung arus terobosan di dalam kapasitor MOS TiN/HfSiO_xN/SiO₂/p-Si. Hasil perhitungan menunjukkan bahwa seiring bertambahnya kecepatan elektron, transmittansi menurun dan arus terobosan berkurang. Arus terobosan menurun seiring dengan bertambahnya ketebalan oksida setara untuk lapisan HfSiO_xN. Saat arah elektron datang menembus penghalang tegak lurus antarmuka lapisan, proses terobosan elektron menjadi lebih mudah. Diperoleh pula bahwa arus terobosan tidak bergantung pada orientasi substrat. Lebih lanjut, model yang diajukan dapat digunakan dalam mendesain divais MOS berkecepatan tinggi dengan arus terobosan yang rendah.

Kata kunci: arus terobosan, kecepatan gerbang, massa anisotropik, material berkonstanta dielektrik tinggi, metode transfer matriks.

Abstract

In this paper, we have developed a model of the tunneling current through a high-dielectric stack in MOS capacitors with anisotropic masses. The transmittance was numerically calculated by employing a transfer matrix method and including longitudinal-transverse kinetic energy coupling which is represented by an electron phase velocity in the gate. The transmittance was then applied to calculate tunneling currents in TiN/HfSiO_xN/SiO₂/p-Si MOS capacitors. The calculated results show that as the gate electron velocity increases, the transmittance decreases and therefore the tunneling current reduces. The tunneling current becomes lower as the equivalent oxide thickness (EOT) of HfSiO_xN layer increases. When the incident electron passed through the barriers in the normal incident to the interface, the electron tunneling process becomes easier. It was also shown that the tunneling current was independent of the substrate orientation. Moreover, the model could be used in designing high speed MOS devices with low tunneling currents.

Keywords: anisotropic mass, gate velocity, high- κ dielectric stack, transfer matrix method, tunneling current

1. Introduction

It is known that silicon-based metal-oxide-semiconductor field-effect transistors (MOSFETs) are aggressively scaled down to the sub-micrometer regime and below for emerging applications required higher speed, higher density and lower voltage operation [1-2]. In order to fulfill these aims, it is required a thinner SiO₂ gate oxide. However, a vast tunneling current arises and power dissipation becomes significantly high when the gate oxide thickness is less than 1.5 nm [3]. In order to solve these problems, it is needed a high- κ dielectric with equivalent electrical oxide thickness but higher physical thicknesses to replace SiO₂ such as Ta₂O₅, Al₂O₃, ZrO₂, HfO₂, La₂O₃, TiO₂, Lu₂O₃, Y₂O₃, SrTiO₃ [4-7]. Since having good dielectric properties and good thermal stability on silicon substrates, Hf-based silicates are the promising candidate to replace SiO₂ compared to other high- κ materials [8-10]. The gate oxides anticipated to substitute SiO₂ are a stack of an ultrathin SiO₂ and a high- κ layer, which is known as a high- κ gate stack, because an ultrathin SiO₂ often grows during fabrication process [3].

Several methods have been utilized to compute a transmittance and tunneling current in high- κ gate stack based MOS capacitors [11-17]. However, they did not consider the coupling effects between longitudinal and transverse motions and anisotropic mass in the MOS capacitors. On the other hand, we have very recently developed an analytical model of tunneling in a TiN/HfSiO_xN/SiO₂/p-Si MOS capacitor by using Airy wave function approach which is including the coupling between longitudinal and transverse motions that is represented by an electron velocity in the metal gate [18]. In addition, it has been experimentally revealed that a high speed MOSFET was realized by injecting high velocity electrons [19].

In this paper, we report a numerical model of transmittance and tunneling current through a high- κ dielectric stack by including a longitudinal-transverse kinetic energy coupling, which is represented by a gate electron-phase velocity and an anisotropic electron mass by using the transfer matrix method. The transfer matrix method has been extensively utilized to study quantum-mechanical tunneling through potential barriers because this method is known to be more accurate than the other method [20]. This model is then applied to calculate the transmittance and tunneling current in TiN/HfSiO_xN/SiO₂/p-Si MOS capacitors. The transmittance and tunneling current as a function of the electron phase velocity in metal gate is studied. In addition, the effect of equivalent oxide thickness (EOT), the incident angle of electron, and the substrate orientation on the transmittance and tunneling current are investigated also.

2. Theoretical Model

We use the model on Ref. [3] in computing tunneling currents in $TiN/HfSiO_xN/SiO2/p-Si$ MOS capacitors. The description of an electron in an anisotropic material under the parabolicband effective mass approximation is used as a first step in the model which is described as [21].

$$\left(\frac{1}{2}\boldsymbol{p}^{T}\frac{\boldsymbol{\alpha}(\boldsymbol{r})}{m_{0}}\boldsymbol{p}+V(z)\right)\Lambda(\boldsymbol{r})=E\ \Lambda(\boldsymbol{r})\ ,$$
(1)

where p is the momentum vector, m_0 is the free-electron mass, $\alpha(\mathbf{r})/m_0$ is the inverse effective-mass tensor, V(z) is the potential energy which is only dependent on the z-direction, E and $\Lambda(\mathbf{r})$ are the electron total energy and wavefunction, respectively. One dimensional Schrödinger-like equation in the z-direction is obtained by employing the separation method.



Figure 1. A potential profile in the z-direction without bias voltage (a) and under a negative bias voltage applied to the TiN gate (b)

Since the total energy *E* is composed of the longitudinal (the *z*-direction) and the transverse (the *x-y* plane) energies is written as $E = E_z + E_{xy}$, the one dimensional Schrödinger-like equation has a coupling term between longitudinal and transverse kinetic energies of electron motion in the anisotropic heterostructure, which is represented by a phase velocity in the TiN gate. Figure 1 gives the potential profile of the conduction band energy in which the conduction band differences between HfSiO_xN and Si, SiO₂ and Si, and TiN and Si are Φ_a , Φ_b , and Δ , respectively, and d_1 and $(d_2 - d_1)$ are the thicknesses of HfSiO_xN and SiO₂, respectively.

The potential profile in Figure 1 is mathematically expressed as

$$V(z) = \begin{cases} 0 & z < 0 \\ (\Phi_{a} + \Delta) - \kappa_{b} p z & 0 \le z < d_{1} \\ (\Phi_{b} + \Delta) + p d_{1} (\kappa_{a} - \kappa_{b}) - \kappa_{a} p z & d_{1} \le z < d_{2} \\ -(eV_{ox} - \Delta) & z \ge d_{2}, \end{cases}$$
(2)

where $p = \frac{eV_{ox}}{\kappa_a(d_2 - d_1) + \kappa_b d_1}$, e is the electronic charge, V_{ox} is the oxide voltage which is

the voltage across the barrier, d_1 and $(d_2 - d_1)$ are the thicknesses of HfSiO_xN and SiO₂, respectively, and κ_a and κ_b are the dielectric constants of HfSiO_xN and SiO₂, respectively.

The potential profile of regions 2 and 3 in Figure 1 under the transfer matrix method (TMM) are divided into *N* segments of rectangular form as described in Figure 2.



Figure 2. The potential profile separated into N segments

Therefore, for the n^{th} segment, the wave function can be approximated as [20]

$$\Theta_{n}(z) = \begin{cases} \{\exp(i\delta_{1}z) + B_{1}\exp(-i\delta_{1}z)\}\exp(-i\gamma_{1}z) & z < 0\\ \{A_{n}\exp(\delta_{n}z) + B_{n}\exp(-\delta_{n}z)\}\exp(-i\gamma_{2}z) & 0 \le z < d_{1}\\ \{A_{n}\exp(\delta_{n}z) + B_{n}\exp(-\delta_{n}z)\}\exp(-i\gamma_{3}z) & d_{1} \le z < d_{2}\\ \{A_{N}\exp(i\delta_{N}z)\}\exp(-i\gamma_{4}z) & z \ge d_{2}, \end{cases}$$
(3)

where A_n and B_n are the amplitudes, δ_1 and δ_N are the wave numbers that are given by

$$\begin{split} \delta_{1} &= \sqrt{\frac{2m_{0}E_{z,1}}{\hbar^{2}}\frac{1}{\alpha_{zz,1}}} \text{ , } \delta_{n} = \left(\frac{2m_{s}}{\hbar^{2}} \left(V_{n} - \sum_{i,j \in \{x,y\}} \frac{\hbar^{2}k_{i}k_{j}}{2m_{0}} \beta_{ij,1} \left(1 - \frac{\beta_{ij,n}}{\beta_{ij,1}}\right) - E_{z,1}\right)\right)^{1/2} \text{ and } \\ \delta_{N} &= \sqrt{\frac{2m_{0} \left(E_{z,1} - \left(V_{N}(z) - \sum_{i,j \in \{x,y\}} \frac{\hbar^{2}k_{i}k_{j}}{2m_{0}} \beta_{ij,1} \left(1 - \frac{\beta_{ij,4}}{\beta_{ij,1}}\right)\right)\right)}{\hbar^{2}} \frac{1}{\alpha_{zz,4}}}{\mu_{n}} \text{ , respectively, } m_{n} \text{ is the } h_{n} \text{ or } h_{n} \text{ o$$

electron effective mass, and V_n and V_N are the constant potential in the nth for 1 < n < N and N^{th} segments. The term of $V_n - \sum_{i,j \in \{x,y\}} \frac{\hbar^2 k_i k_j}{2m_0} \beta_{ij,l} \left(1 - \frac{\beta_{ij,n}}{\beta_{ij,l}} \right) - E_{z,1}$ is known as the effective potential.

By applying the boundary conditions [22] of successive segments, it is easily obtained the total transfer matrix for N segments as follows:

$$\binom{1}{B_1} = M_{12}M_{23}M_{34}\dots M_{N-1N}\binom{A_N}{0},$$
(4)

where M_{nn+1} is a matrix relating A_n and B_n with A_{n+1} and B_{n+1} .

Articulating
$$M_{12}M_{23}M_{34}\dots M_{N-1N}$$
 as $\Gamma = \begin{pmatrix} \Gamma_{11} & \Gamma_{12} \\ \Gamma_{21} & \Gamma_{22} \end{pmatrix}$, the term *f* becomes
 $f = A_N = \frac{1}{\Gamma_{11}}$. (5)

The transmittance $T(E_z)$ is found as

$$T(E_z) = \frac{\delta_N \alpha_{zz,4}}{\delta_1 \alpha_{zz,1}} f.f^*.$$
(6)

The electron transmittance is then employed to calculate the tunneling current which is given by [18]

$$J_{z} = \frac{e \sum_{l} n_{vl} m_{dl}}{2\pi^{2} \hbar^{3}} \times \int_{0}^{\infty} T(E_{z}) kT \ln \left\{ \frac{\left(1 + \exp\left[(E_{F} - E_{z})/kT\right]\right]^{\lambda}}{1 + \exp\left[(E_{F} - E_{z} + (\Delta - eV_{ox}))/kT\right]} \right\} dE_{z}, \quad (7)$$

where n_v and m_d are the number of valleys and the density of states mass, respectively, $T(E_z)$ is the electron transmittance, *k* is the Boltzmann constant, and *T* is the temperature. The tunneling current in Equation (7) is easily computed by using the Gauss–Laguerre Quadrature method [23].

3. Results and Discussion

In present model, the quantization effect of the p-type Si accumulation layer is negligible to avoid requiring more numerical treatment. In order to calculate the transmittance and tunneling current, it was used the following parameters: Φ_a = 1.52 eV, Φ_b = 3.34 eV, Δ = 0.53 eV, d_1 = 0.5 nm, κ_a = 13.5, κ_b = 3.9 [18]. The electron effective mass in the HfSiO_xN and TiN metal gate are considered to be isotropic and taken as 0.20 m_0 and m_0 , respectively [13, 24]. The

electron effective mass in SiO₂ is also isotropic and assumed to be m_0 for the thickness of 0.5 nm as extrapolated from Ref. [12]. Since there are six equivalent valleys in the conduction band of Si which are divided into three groups of valleys, the tensor elements of Si(100), Si(110), and Si(111) are given in Tables 1, 2, and 3, respectively [25-27].

Table 1. Tensor elements αij of Si(100)			Table 2. Tensor elements αij of Si(110)				
Valley		Si(100)		Valley		Si(110)	
	1.02	0	0		5.26	0	0
V1	0	5.26	0	V1	0	3.14	2.12
	0	0	5.26		0	2.12	3.14
V2	5.26	0	0	V2	5.26	0	0
	0	1.02	0		0	3.14	-2.12
	0	0	5.26		0	-2.12	3.14
	5.26	0	0	V3	1.09	0	0
V3	0	5.26	0		0	5.26	0
	0	0	1.02		0	0	5.26

Table 3. Tensor elements αij of Si(111)							
Valley	ey Si(111)						
	4.57	1.21	0.98				
V1	1.21	3.14	-1.70				
	0.98	-1.70	3.87				
	4.57	-1.21	0.98				
V2	-1.21	3.14	1.70				
	0.98	1.70	3.87				
	2.48	0	-1.97				
V3	0	5.26	0				
	-1.97	0	7.74				

Figure 3 compares the transmittances of electrons in the valley 1 (V1) moving from the metal gate with phase velocities in the range of $1.5 - 2.5 \times 10^5$ m/s and tunneling through a barrier composed of 2.5 nm-thick HfSiO_xN and 0.5 nm-thick SiO₂ layers for the oxide voltage of 1 V with that without considering the coupling effect of the longitudinal and transversal kinetic energies.



Figure 3. Electron transmittance in the MOS capacitor calculated without and with considering the coupling effect





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It is seen that the transmittances increase as the incident electron energy increases. For incident electron energies higher than the barrier height, the transmittances show oscillatory behaviors as given in the inset. It is also shown that the transmittance calculated without the coupling effect is much higher than those obtained by including the gate electron phase velocities in the range of 1.5 to 2.5×10^5 m/s. In addition, the increase of the gate electron velocity reduces the transmittance. This is because the gate electron velocity causes an effective potential barrier $V_{eff}(z)$ higher than the barrier V(z) as already explained in Refs. [3, 28-29].

Employing the same parameters as used in obtaining Figure 3, electron tunneling current density, which is contributed from each valley, was calculated. The tunneling current density as a function of oxide voltage is depicted in Figure 4. It is found that the tunneling current density increases with the oxide voltage. In addition, the electron tunneling process from the metal gate to the silicon substrate does not happen for the oxide voltages lower than 0.53 V as demonstrated in Figure 1 because electrons in the Fermi level of TiN metal gate can not occupy the forbidden states of p-Si substrate band gap [18]. The tunneling current density calculated without the coupling effect of longitudinal-transverse kinetic energy is the highest as compared to those obtained by including the coupling effect. It is also found that the increase of the gate electron velocity reduces the tunneling current density. This is because the barrier height V(z) in Equation (2) is enhanced by the increase of the gate electron phase velocity. The transmittance is, therefore, reduced. Moreover, the slopes of the curves in Figure 4 are almost identical. This finding means that the modification of potential barrier can be compensated for the electron effective mass of the tunneling barrier. This is an indication that the inclusion of the gate electron phase velocity in the present model could be used to explain the achievement of a high-speed source-heterojunction MOS transistor due to the injection of high velocity electrons as reported in Ref. [19].



Figure 5. The relation between the tunneling current and equivalent oxide thickness (EOT) in the MOS capacitor TiN/HfSiOxN/SiO2/p-Si(100)



Figure 6. The electron transmittance versus the incident angle of electron

To verify the effects of longitudinal-transverse kinetic energy coupling and equivalent oxide thickness (EOT) to tunneling current, the calculated tunneling currents for a stack of 0.5 nm interfacial SiO_2 and $HfSiO_xN$ with and without the coupling effect are demonstrated in Figure 5. It is found that the coupling effect can not be neglected in tunneling current in the MOS capacitor for thick $HfSiO_xN$ layer. Moreover, as the EOT (effective oxide thickness) of $HfSiO_xN$ layer increases, the tunneling current becomes lower. It means that the usage of $HfSiO_xN$ as the gate oxide can reduce the tunneling current as explained in Refs. [3] and [18].

Figure 6 depicts the electron transmittance as a function of the incident angle of electron (in degree) with respect to the barrier over the range from -80° to 80° in 2.5 nm-thick HfSiO_xN and 0.5 nm-thick SiO₂ layers with and without including the coupling effect of the

longitudinal-transverse kinetic energy. In this problem, we use the coordinate system as shown in Figure 7. Noting that the total energy *E* is composed of E_{xy} and E_z , where E_{xy} and E_z are the transverse and longitudinal energies, respectively, the total energy becomes

$$E = \frac{\hbar^2}{2m_0} \left\{ \left(\frac{v_e m_0}{\hbar} \right)^2 \sum_{i, j \in \{x, y\}} \frac{1}{\beta_{ij}} + \left[\alpha_{zz} k^2 \cos^2 \theta + 2 \left(\alpha_{yz} k^2 \sin^2 \theta \cos \theta \sin \varphi + \alpha_{zx} k^2 \sin \theta \cos \theta \cos \varphi \right) \right] \right\}.$$
(8)

We calculate the direct transmittance with the angles of incident electron θ and φ . We fix φ to $\pi/2$ and change only θ for simplicity. From the figure, it is shown that the transmittance decreases as the gate electron phase velocity increases as also given in Figure 4. It is also shown that the transmittance gives the highest value at the incident angle of 0° (normal incident to the interface). It means that for the incident angle of 0°, the movement of electron becomes easier. These results are the same as those obtained with the analytical method given in Ref. [30].



Figure 7. The coordinate system used in this problem



Figure 8. The tunneling current in the TiN/HfSiO_xN/SiO₂/p-Si MOS capacitor for different substrate orientations

Figure 8 demonstrates the effect of substrate orientation to the electron tunneling current. Here, it was used the electron incident angle of 0° and the gate electron velocity of 10° m/s. It is seen that the tunneling currents give the same result for the substrate orientations of Si(100), Si(110), and Si(111). It means that the tunneling current in the TiN/HfSiO_xN/SiO₂/p-Si capacitor is not affected by the substrate orientation. The result is also consistent with that, which obtained analytically as given in Ref. [3].

4. Conclusion

We have studied numerically the electron tunneling current density TiN/HfSiO_xN/SiO₂/p-Si MOS capacitor. The transmittance through the barrier is derived by including the effect of a longitudinal-transverse kinetic energy coupling represented by the gate electron phase velocity. It has been shown that the increase of the gate electron velocity enhances the barrier height, decreases the transmittance, and therefore reduces the tunneling current density. It has also been shown that the tunneling current becomes lower as the EOT increases. In addition, it was found that the tunneling process in TiN/HfSiO_xN/SiO₂/p-Si capacitor becomes easier when the incident electron is normal to the interface. It was also found that the tunneling current does not

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depend on the substrate orientation. Furthermore, the present results suggest that the MOS device could operate at a high speed with a low tunneling current when electrons from the gate could be injected with a high phase velocity.

Acknowledgment

This work was supported by a research grant from the Asahi Glass Foundation in the fiscal year of 2011

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